

LPE Reading Group Meeting
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dhcp-228.cds.caltech.edu

Attributes of Distr. Algor.

① IPC

② Timing Model

completely
synchronous

Partially
syn.

asynchro.

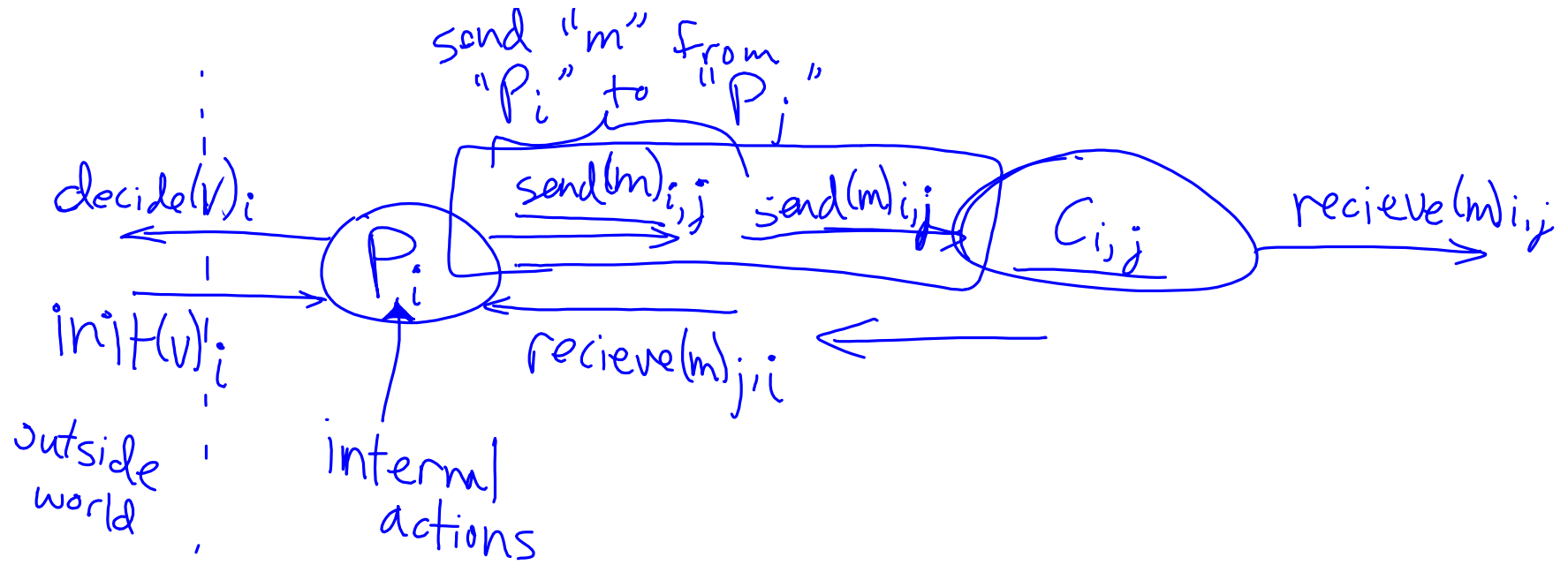
Ch. 8. - I/O Automata

③ Failure Model

④ Problems Addressed

I/O Automata

- General - gives precise way of reasoning about system components
- Fairness - each process gets infinite chances on an infinite time scale



- I/O Autom. - 5 components (A) ! ① aids in error ID.
- ① $\text{sig}(A) - S = (\text{in}(A), \underbrace{\text{out}(A), \text{int}(A)}_{\text{local}(A)})$ ② helps theory
- ② $\text{states}(A)$ input-enabled
- ③ $\text{start}(A)$ (req. $\neq \emptyset$) (s, π, s')
- ④ $\text{trans}(A) \subseteq \text{states}(A) \times \text{act}(\text{sig}(A)) \times \text{states}(A)$
- ⑤ $\text{tasks}(A)$; equivalence relation on $\text{local}(\text{sig}(A))$
(countably many)

Example - Channel C_{ij}
 M - fixed message alphabet

① Sig (C_{ij}) :

Input:

send $(m)_{ij}$
 $m \in M$

Output:

recieve $(m)_{ij}$
 $m \in M$

1
trans.

④ trans (C_{ij})

⑤ tasks (C_{ij})

send $(m)_{ij}$ { recive $(m)_{ij} : m \in M$ }
 Effect:
 add "m" to queue

receive $(m)_{ij}$

Precondition: m is 1st on queue
 Effect: remove m

②, ③ states (C_{ij}) ;
 FIFO

queue (elements of M), initially empty

Example P_i (n processes), $f: V^n \rightarrow V$ V : fixed value set
 and $\emptyset \neq V$

From outside world \rightarrow h_i decide \leftarrow sent to world

From local $P_j, j \neq i$ \rightarrow receive send \leftarrow sent to particular $P_j, j \neq i$