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Loop gain limitations	
• Poles of closed loop = poles of 1 + L. Suppose $C = k n_c/d_c$, where k is the gain of the controller	
$1 + L = 1 + k \frac{n_c n_p}{d_c d_p} = \frac{d_c d_p + k n_c n_p}{d_c d_p}$	
 For large k, closed loop poles approach open loop zeros RHP zeros limit maximum gain ⇒ serious design constraint! 	
 Root locus interpretation Plot location of eigenvalues as a function of the loop gain k Can show that closed loop poles from open loop poles (k = 0) to op loop zeros (k = \infty) More details in CDS 110 on Wed 	go $\frac{1}{2}$ \frac
24 Nov 03 R. M. Mu	rray, Caltech CDS 12



